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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	Unknown <u>10820427</u>
		Filing Date	April 7, 2004
		First Named Inventor	Francesco CRETTI
		Art Unit	Unknown
		Examiner Name	Unknown
		Attorney Docket Number	2110-110-3
Sheet	1	of	1

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
J-L	AA	US- 5,057,838	10/15/91	Kazuhiro Tsuji et al.	
J-L	AB	US- 6,118,398	09/12/00	Gregory J. Fisher et al.	
J-L	AC	US- 6,426,715 B1	07/30/02	Jan Roelof Westra et al.	
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS		
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
J-L	AD	GEERT A.M. VAN DER PLAS, et al.; "A 14-bit Intrinsic Accuracy Q <sup>2</sup> Random Walk CMOS DAC"; IEEE Journal of Solid-State Circuits; December 1999; pgs. 1708-1718; Vol. 34, no. 12; IEEE; New York
J-L	AE	YONGHUA CONG et al.; "Switching Sequence Optimization for Gradient Error Compensation in Thermometer-Decoded DAC Arrays"; IEEE Transaction on Circuits and Systems - II: Analog and digital Signal Processing; July 2000; pgs. 585-595; Vol. 47, no. 7; IEEE; New York
J-L	AF	European Search Report dated September 24, 2003
Examiner Signature	<u>[Signature]</u>	
	Date Considered	<u>12/03/2004</u>

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